

FIG. 1

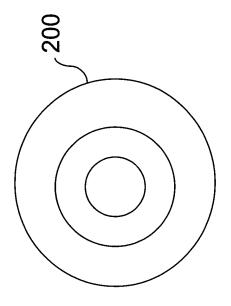
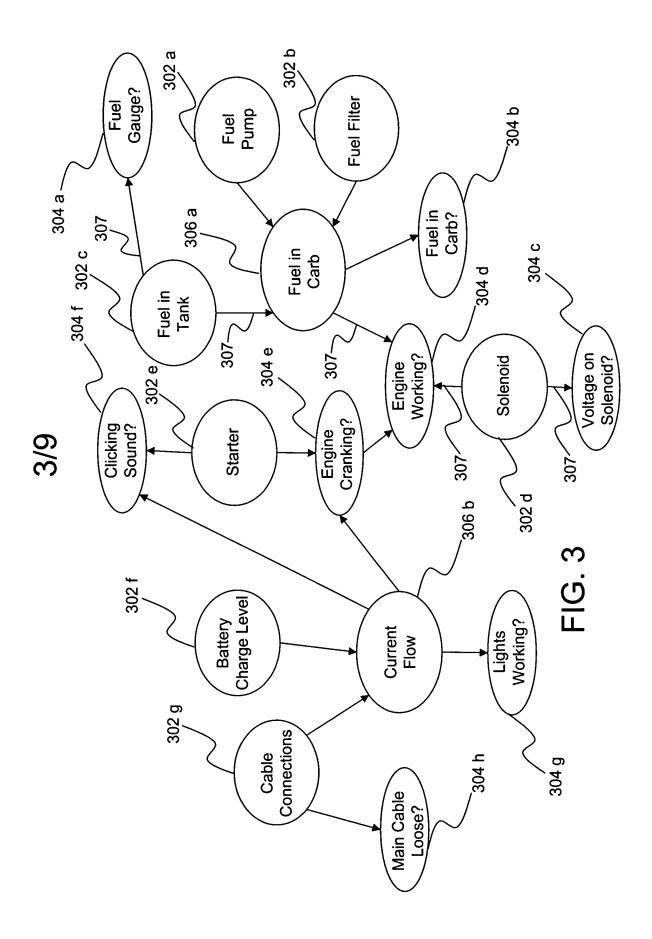
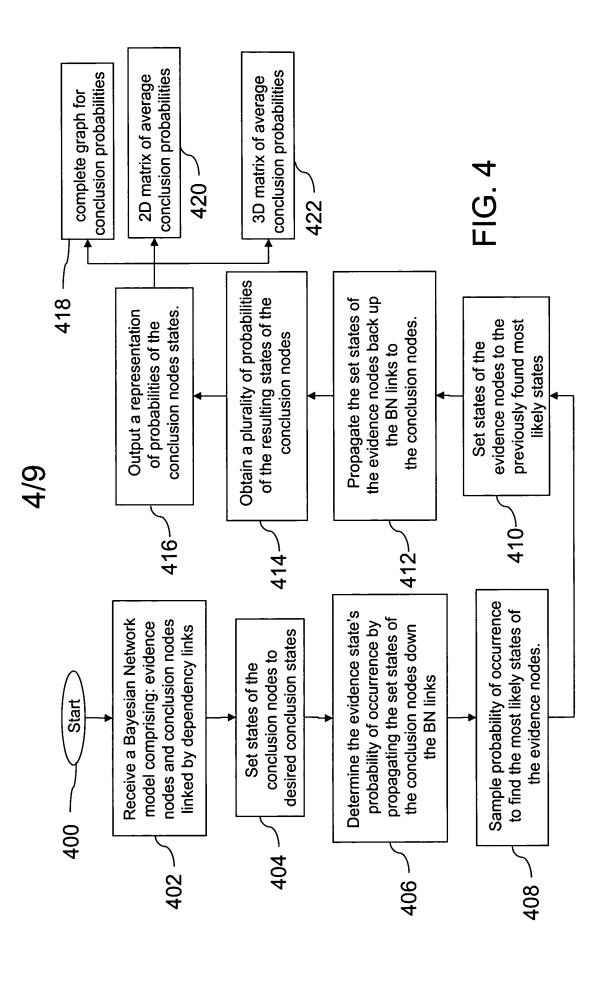


FIG. 2





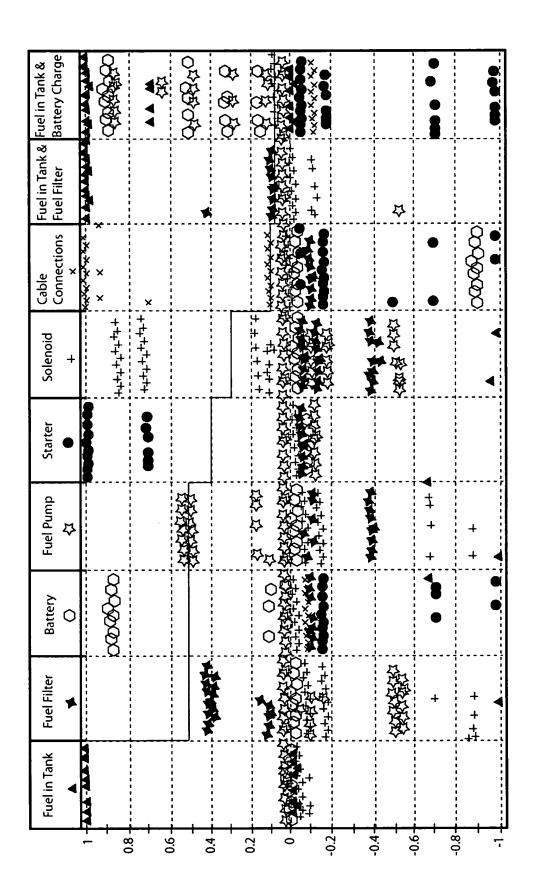


FIG. 5

IMPLIED FAILURES

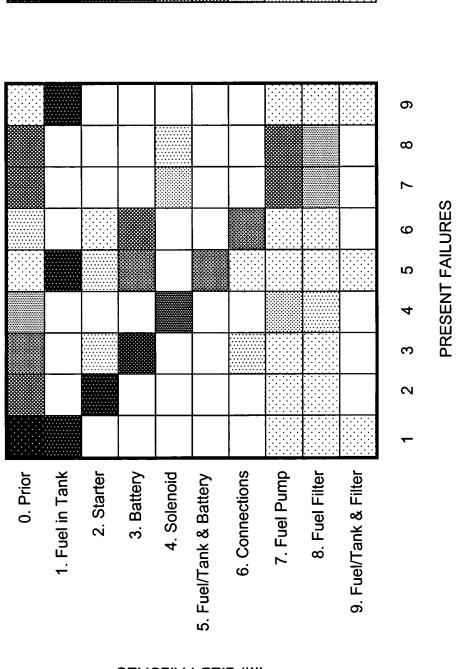


FIG. 6

